

JC109 MAY 26 2004 U.S. PATENT & TRADEMARK OFFICE		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. 852463.406	APPLICATION NO. 10/727,138
				APPLICANTS Kaushik Saha et al.	
				FILING DATE December 3, 2003	GROUP ART UNIT 2124 2193
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)					

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
CD	AA	5,293,330	03/08/94	Sayegh	364	726	
CD	AB	6,366,936	04/02/02	Lee et al.	708	404	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION
					YES NO
	AK				
	AL				
	AM				
	AN				
	AO				

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

CD	AP		Piedra, R., "Parallel 1-D FFT Implementation with TMS320C4x DSPs," <i>Application Report, Texas Instruments, Inc.</i> , pp. 1-68, February 1994.
	AQ		
	AR		

EXAMINER	/Chat Do/ (01/16/2007)	DATE CONSIDERED	01/16/2007
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* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).